I.   IN THE CLAIMS

1.   (Previously Presented) A remote control, comprising:

a memory pre-programmed with addresses and commands for a plurality of electronic devices for a home theatre system;

a processor configured for communicating with the memory to access the addresses and commands for the plurality of electronic devices, and for storing in the memory a plurality of signals encoded with the respective addresses and commands;

an initiation device capable of communicating with the processor so that when the initiation device is activated the processor encodes an address and a command into a respective one of the signals for each electronic device in the plurality of electronic devices; and

a plurality of transmitters capable of communicating with the processor where the processor directs the transmitters to simultaneously and automatically send the respective signals to the plurality of electronic devices.

2.   (Previously Presented) The remote control according to claim 1, further including an input device capable of receiving an address and a command for an electronic device from a memory storage area.

3.   (Previously Presented) The remote control according to claim 1, further including an output device capable of communicating with the processor and displaying information about a status of the remote control.

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4.-13. (Canceled)

14. (Previously Presented) The remote control according to claim 1, where the plurality of electronic devices includes a TV.

15. (Previously Presented) The remote control according to claim 1, where the plurality of electronic devices includes a DVD player.

16. (Previously Presented) The remote control according to claim 1, where the plurality of electronic devices includes an amplifier.

17.-20. (Canceled)

21. (Previously Presented) A method for controlling electronic devices, comprising:

activating a dedicated button;

cycling through a plurality of addresses in a memory to ascertain an address pre-programmed for a corresponding one of a plurality of electronic devices for a home theatre system;

if the ascertained address is found for the corresponding electronic device in the plurality of electronic devices, then encoding the address and a command into a turn on or off signal for the corresponding electronic device;

repeating the cycling and encoding steps for each of the plurality of electronic devices to encode a plurality of respective turn on or off signals; and

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simultaneously transmitting the respective turn on or off signals to the plurality of electronic devices via a plurality of transmitters.

22.   (Previously Presented) The method according to claim 21, further including:

if the address for electronic device is not available in the memory, then determining if a default address is available for the electronic device;

if a default address is available for the electronic device, then encoding the default address and a command into a signal for the electronic device; and

if a default address is not available for the electronic device, then cycling to a next electronic device in the plurality of electronic devices.

23-32. (Canceled)

33.   (Previously Presented)   The remote control of claim 1, where the initiation device includes a dedicated button capable of communicating with the processor so that when the dedicated button is activated the processor encodes an address and a turn on or off command into a signal for each respective electronic device.

34.   (Previously Presented) The remote control of claim 1, where the initiation device includes a dedicated on button capable of communicating with the processor so that when the dedicated on button is activated the processor encodes an address and a turn on command into a signal for each respective electronic device, and a dedicated off button capable of communicating with the processor so that when the dedicated off button is activated the

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processor encodes an address and a turn off command into a signal for each respective electronic device.

35.   (Previously Presented) The remote control of claim 1, where the processor is further configured for:

cycling through a plurality of addresses in the memory to ascertain an address pre-programmed for a corresponding one of the plurality of electronic devices;

if the ascertained address is found for the corresponding electronic device in the plurality of electronic devices, then encoding the address and a command into a turn on or off signal for the corresponding electronic device; and

repeating the cycling and encoding steps for each of the plurality of electronic devices to encode a plurality of respective turn on or off signals.

36.   (Previously Presented) The remote control of claim 35, where the processor is further configured for:

if the address for electronic device is not available in the memory, then determining if a default address is available for the electronic device;

if a default address is available for the electronic device, then encoding the default address and a command into a signal for the electronic device; and

if a default address is not available for the electronic device, then cycling to a next electronic device in the plurality of electronic devices.

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37. (Previously Presented) A system for controlling a plurality of electronic devices, the system comprising:

a plurality of electronic devices for a home theatre system where each of the plurality of electronic devices is associated with a corresponding one of a plurality of addresses; and

a remote control including:

a memory pre-programmed with respective addresses and commands for the plurality of electronic devices;

a processor configured for communicating with the memory to access the addresses and commands for the plurality of electronic devices, and for storing in the memory a plurality of signals encoded with the respective addresses and commands;

an initiation device capable of communicating with the processor so that when the initiation device is activated the processor encodes an address and a command into a respective one of the signals for each electronic device in the plurality of electronic devices; and

a plurality of transmitters capable of communicating with the processor where the processor directs the transmitters to simultaneously and automatically send the respective signals to the plurality of electronic devices.

38. (Previously Presented) The system of claim 37, further including an input device capable of receiving an address and a command for an electronic device from a memory storage area.

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39.   (Previously Presented) The system of claim 37, further including an output device capable of communicating with the processor and displaying information about a status of the remote control.

40.   (Previously Presented) The system of claim 37, where the initiation device includes a dedicated button capable of communicating with the processor so that when the dedicated button is activated the processor encodes an address and a turn on or off command into a signal for each respective electronic device.

41.   (Previously Presented) The system of claim 37, where the initiation device includes a dedicated on button capable of communicating with the processor so that when the dedicated on button is activated the processor encodes an address and a turn on command into a signal for each respective electronic device, and a dedicated off button capable of communicating with the processor so that when the dedicated off button is activated the processor encodes an address and a turn off command into a signal for each respective electronic device.

42.   (Previously Presented) The system of claim 37, where the processor is further configured for:

cycling through a plurality of addresses in the memory to ascertain an address pre-programmed for a corresponding one of the plurality of electronic devices;

if the ascertained address is found for the corresponding electronic device in the plurality of electronic devices, then encoding the address and a command into a turn on or off signal for the corresponding electronic device; and

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repeating the cycling and encoding steps for each of the plurality of electronic devices to

encode a plurality of respective turn on or off signals.

43.   (Previously Presented) The system of claim 37, where the processor is further configured

for:

if the address for electronic device is not available in the memory, then determining if a

default address is available for the electronic device;

if a default address is available for the electronic device, then encoding the default

address and a command into a signal for the electronic device; and

if a default address is not available for the electronic device, then cycling to a next electronic

device in the plurality of electronic devices.

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